



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani, et al.

Art Unit: 2811

Serial No.: 08/958,568

Examiner : S Hu

Filed

: October 28, 1997

Title: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING THE SAME

Assistant Commissioner for Patents

Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed. The required fees are computed below. Please apply any charges not covered, or any credits, to Deposit Account No. 06-1050.

Total Claims 66 49 Independent 11 7 Applicant hereby petitions under 37 C.F.R. \$1.136 for a 1 month extension of time. TOTAL FEE DUE

110 \$728

\$306

312

A check for \$728 is attached.

Please apply any charges or credits to Deposit Account

No. 06-1050.

PTO Customer No. 20985

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10022066.doc

Respectfully submitted,

17

Harris

Reg. No. 32,030

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

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Susan M. Regan

Typed or Printed Name of Person Signing Certificate



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RESPONSE TO RESTRICTION REQUIREMENT

In response to the Official Action mailed February 3, 2000, applicants elect the invention of claims 13, 14, and 19-28, Group 1 covering the Embodiment of Figures 1(A)-1(F). The election is made without traverse.

In the Claims:

Please add the following new claims.

56. An electro-optical device comprising:

a substrate;

a thin film transistor provided over said substrate in a

pixel; and

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01 FC:102 02 FC:103 03 FC:115 312.00 OP 306.00 OP 110.00 OP CERTIFICATE OF MAILING BY FIRST CLASS MAIL

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